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Application/Control Number: 09/784,087

Art Unit: 2871

## **DETAILED ACTION**

## Allowable Subject Matter

- 1. Claims 1-5 and 21-22 are allowed.
- 2. The following is an examiner's statement of reasons for allowance:

The prior art of record neither discloses nor teaches that a liquid crystal display device comprising various elements, more specifically, as the following:

the pixel electrode electrically connected to the drain electrode via inner side surface of the drain electrode and in direct contact with a portion of the substrate [claim 1; as shown in Fig.6D];

the drain electrode has a through hole that exposes a portion of the first insulating layer, and the pixel electrode electrically contact an inner side surface of the drain electrode via the through hole [claim 3; as shown in Fig.6D];

the data pad contact hole passing through the doped amorphous silicon layer and passing through the amorphous silicon layer, and the data pad electrode electrically contact an inner side surface of the data pad via the data pad contact hole [claim 4; as shown in Fig.6D].

Claim 2 is dependent on claim 1; claim 21 is dependent on claim 3; claims 5 and 22 are dependent on claim 4.

The closest references Applicant admitted prior art and US 6,163,356 (Song et al) discloses a structure of a liquid crystal display device comprising a drain contact hole, a source pad contact hole (data pad contact hole) and a gate pad contact hole, but the prior art of record do not disclose the drain contact hole and the data pad (source

Application/Control Number: 09/784,087

Art Unit: 2871

pad) contact hole passing through the silicon layer (the pure amorphous silicon layer and the doped amorphous silicon layer), wherein the portions of the gate insulating layer are exposed and the pixel electrode or the data pad electrode contact the inner side surface of the drain electrode or the inner side surface of the data pad via the through holes as claimed in the claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mike Qi whose telephone number is (571) 272-2299. The examiner can normally be reached on M-T 8:00 am-5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim can be reached on (571) 272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Application/Control Number: 09/784,087

Art Unit: 2871

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Mike Qi December 6, 2004

TARIFUR R. CHOWDHURY